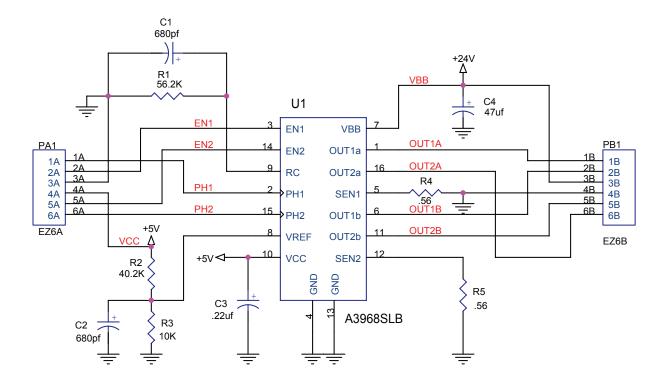


Demo Board Schematic/Layout

SCHEMATIC



APEK3968SLB-01-DB November 11, 2016

BILL OF MATERIALS

BOARD, DEMO, A3968SLB Revised: May 19, 2000

76-0915-000-BOM Revision: 1

Allegro MicroSystems

Bill Of Materials

Page 1 OF 1

Item	Qty	Reference Part	Description	Stock Number
1	2	C2,C1 680pf	Murata, 6C0G681J50V Surface Mount Cap	101923
2	1	C3 0.1uf	Murata, 6X7R104K50V Surface Mount Cap	101380
3	1	C4 47uf	Panasonic, ECE-V1VA470P VA Series 35V SMT Ca	ap
4	1	R1 56.2K	Dale, CRCW1206-101 SMT RESISTOR	
5	1	R2 40.2K	Dale, CRCW1206-101 SMT RESISTOR	
6	1	R3 10K	Dale, CRCW1206-101 SMT RESISTOR	
7	2	R5,R4 .56	Panasonic, ERJ-1WRQJxxx 1w, 2512, 5%, SMT R	ESISTOR
8	1	U1 A3968SLB	Allegro, Dual Full-bridge PWM Motor Driver 16-lead batwing SOIC	
9	1	PCB 76-0785	Board, Demo, A3966SLB - Use PC Board	



APEK3968SLB-01

Demo Board Schematic/Layout

Revision History

Number	Date	Description
_	November 11, 2016	Initial release

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